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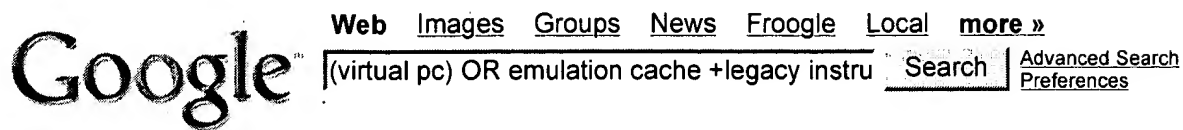
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Web Results 1 - 100 of about 91,200 for **(virtual pc) OR emulation cache +legacy instructions** with Safesearch

Download details: Microsoft **Virtual PC** 2004 45-Day Free Trial Edition

Overview; System Requirements; **Instructions**; Similar Downloads ... Other: Level-2

cache and CD-ROM required **Virtual PC** 2004 runs on: Windows 2000 ...

go.microsoft.com/?LinkId=318597 - 23k - [Cached](#) - [Similar pages](#)

WindowsDevCenter.com: Run Multiple OSES with **Virtual PC** and VMware ...

For example, neither permitted access to a **legacy** floppy disk drive through the

guest. ... One difference is that **Virtual PC** requires a level 2 **cache**. ...

www.windowsdevcenter.com/pub/a/windows/2005/06/21/virtual_OS.html - 42k - Jul 12, 2005 -

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Strobe Data high performance **virtual** disk. **emulation** available ... Caching ¾

Future **cache emulation** planned using Windows NT/2000 memory resources ...

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... I/O supported via **legacy** bus. **Emulation** exists for **virtual** BACI. ... Caching ¾

Future **cache emulation** planned using Windows NT/2000 memory resources ...

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Ars Technica: An Introduction to 64-bit Computing and x86-64 ...

The first and least interesting mode is **legacy** mode. When in **legacy** mode, the

processor functions ... **Virtual** machine shootout: **Virtual PC** vs. VMware ...

arstechnica.com/cpu/03q1/x86-64/x86-64-4.html - 25k - Jul 12, 2005 - [Cached](#) - [Similar pages](#)

Ars Technica: A Brief Look at the PowerPC 970 - Page 1 - (10/2002)

The 970 fetches eight **instructions** per cycle from its 64KB instruction **cache** into

an instruction queue. ... **Virtual** machine shootout: **Virtual PC** vs. VMware ...

arstechnica.com/wankerdesk/3q02/powerpc.html - 31k - [Cached](#) - [Similar pages](#)

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Emulation and cross-development for PowerPC

... even a cycle-accurate emulator can omit **cache** or IO **emulation**, ... **Virtual PC**,

a commercial emulator, is used by a large number of Macintosh,® owners to ...

www-106.ibm.com/developerworks/library/pa-emulation.html - 40k - [Cached](#) - [Similar pages](#)

[PDF] A Unified View of **Virtual** Machines A Unified View of **Virtual** ...

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Virtual Machine "requirements". 1. All innocuous **instructions** are executed by

... **cache** (**emulation** s/w. sets up links). 2. Change protections on ...

www.cs.purdue.edu/homes/jv/vee/smith.pdf - [Similar pages](#)

EP1107122 Trw european software patent - **Legacy** mil-std-1750a ...

EP1107122 Trw inc (US): **Legacy** mil-std-1750a software emulator address translation

... To work around **instructions** set incompatibilities, **emulation** systems ...

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"09/992121"	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 14:54
S2	30	(btree or b-tree or (balanced near2 tree)) and "703".clas.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 14:59
S3	21	S2 and simulat\$4	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 14:58
S4	2	S3 and legacy	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 14:58
S6	1	((btree or b-tree or (balanced near2 tree)) same (simulat\$4 or emulat\$4)).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:00
S7	7	((btree or (b adj2 tree) or (balanced near2 tree)) same (simulat\$4 or emulat\$4 or model\$4)).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:01
S8	7946	"703".clas.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:01
S10	1133	S8 and ((linked near3 list) or tree)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:02

S12	12	S10 and ((translate\$ or convert\$) near5 architecture\$1)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:03
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L1	0	((Bayer adj2 tree) and (emulat\$4 or simulat\$4 or (virtual near3 (pc computer))))).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 09:26
L2	8438	((memory cache tree (linked near3 list)) and (emulat\$4 or simulat\$4 or (virtual near3 (pc computer))))).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 09:48
L3	484	2 and "703".clas.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 09:27
L6	11	((memory cache tree (linked near3 list)) and (emulat\$4 or simulat\$4 or (virtual near3 (pc computer machine))) and (translate or convert) near3 (instruction code opcode)).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 10:44
L7	1	09/992121	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 10:44
S1	42	(legacy near3 instruction) and emulat\$4	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 15:51
S3	10	((simulat\$4 or emulat\$4) with (instruction)) same (cache or memory)).ti. and (cache near3 instructions)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 15:52
S4	5	S3 and (group\$3 or block\$3)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:18

S8	75	((link OR list OR group OR block)same cache same simulat\$4) and "703".clas.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:22
S9	44	((link OR list OR group OR block)same cache same (simulat\$4 or emulat\$4)).ab.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:23
S13	744	emulat\$4 and target and architecture and legacy	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:25
S14	703	S13 and (cache or memory)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:26
S15	47	S14 and "703".clas.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 16:26
S16	5	("5222224" "5319760" "5511217" "5553291" "5893144").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/13 16:41
S17	365	vsam	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/13 16:41
S18	3	S17 and "703".clas.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/13 16:41
S19	6	("4779188" "5381535" "5414851" "5426748" "5459872" "6253224").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/13 16:47